

IN THE CLAIMS:

Please amend claims 7, 10, 13, 17, 31, 34, 36, 41 and 46 as indicated in the complete listing of claims provided below.

Sub
C1
1-6. (canceled)

7. (currently amended) A method of simulating a second font utilizing a first font, the method comprising:
automatically stripping a top line and a bottom line from the first font to simulate the second font;
wherein the first font comprises an $n \times (m+2)$ font and the second font comprises an $n \times m$ font.

B1
8. (previously presented) The method of claim 7 wherein:
the first font comprises a 9×16 font; and
the second font comprises a 9×14 font.

9. (previously presented) The method of claim 7 wherein the first font comprises a 8×16 font; and the second font comprises a 8×14 font.

10. (currently amended) The method of claim 7 further comprising:
copying the $n \times (m+2)$ font from BIOS into memory to simulate the second font.

11-12. (canceled)

13. (currently amended) A machine-readable medium containing a plurality of executable instructions, which when executed on a processor cause said processor to perform a method of emulating a second font utilizing a first font, the method comprising:
automatically stripping a top line and a bottom line from the first font to emulate the second font;
wherein the first font comprises an $n \times (m+2)$ font and the second font comprises an $n \times m$ font.
14. (previously presented) The machine-readable medium of claim 13 wherein $m = 14$.
- 15-16. (canceled)
17. (currently amended) An apparatus for emulating a second font utilizing a first font, comprising:
automatically stripping a top line and a bottom line from the first font to emulate the second font;
wherein the first font comprises an $n \times (m+2)$ font and the second font comprises an $n \times m$ font.
18. (previously presented) The apparatus of claim 17 wherein $m = 14$.
19. (canceled)
20. (previously presented) A system, comprising:
a BIOS memory, the BIOS memory storing a first font and instructions; and
a processor coupled to the BIOS memory, the processor emulating a second font utilizing the first font in response to the instructions.

21. (previously presented) The system of claim 20 wherein:
the processor emulating the second font by stripping a portion from the first font in
response to receiving an access request for the second font.
22. (previously presented) The system of claim 20 further comprising:
a first memory coupled to the processor, the processor copying the first font from the
BIOS memory into the first memory to emulate the second font.
23. (previously presented) The system of claim 21 wherein:
the portion comprises a top line and a bottom line of an $n \times (m+2)$ font.
24. (previously presented) The system of claim 23 wherein:
the second font comprises an $n \times m$ font.
- 25-30. (canceled)
31. (currently amended) A method of simulating a second font comprising a set of
characters utilizing a first font comprising a set of characters, the method comprising:
automatically stripping a top line of each character of the first font; and
automatically stripping a bottom line of each character of the first font;
wherein the first font comprises an $n \times (m+2)$ font and the second font comprises an
 $n \times m$ font.
32. (previously presented) The method of claim 31 wherein $m = 14$.
33. (previously presented) The method of claim 32 wherein n is one of: 8 and 9.

34. (currently amended) The method of claim 31 further comprising:
copying the first font from BIOS into memory to simulate the second font.
35. (previously presented) A method to start a data processing system, the method comprising:
emulating a second font utilizing a first font stored in a BIOS memory of the data processing system in response to instructions stored in the BIOS memory.
36. (currently amended) The method of claim 35, further comprising:
copying the first font from the BIOS memory to a first memory of the data processing system to emulate the second font.
37. (previously presented) The method of claim 35, wherein said emulating comprises:
stripping a portion from the first font.
38. (previously presented) The method of claim 37, wherein the portion comprises a top line of the each character of the first font and a bottom line of each character of the first font.
39. (previously presented) The method of claim 38, wherein the second font is of two lines of pixels shorter than the first font.
40. (previously presented) A machine-readable medium containing a plurality of executable instructions, which when executed on a processor cause said processor to perform a method to start a data processing system, the method comprising:

emulating a second font utilizing a first font stored in a BIOS memory of the data processing system in response to instructions stored in the BIOS memory.

41. (currently amended) The medium of claim 40, wherein the method further comprises: copying the first font from the BIOS memory to a first memory of the data processing system to emulate the second font.
42. (previously presented) The medium of claim 40, wherein said emulating comprises: stripping a portion from the first font.
43. (previously presented) The medium of claim 42, wherein the portion comprises a top line of the each character of the first font and a bottom line of each character of the first font.
44. (previously presented) The medium of claim 43, wherein the second font is of two lines of pixels shorter than the first font.
45. (previously presented) A data processing system, comprising:
means for emulating a second font utilizing a first font stored in a BIOS memory of the data processing system in response to instructions stored in the BIOS memory.
46. (currently amended) The data processing system of claim 45, further comprising:
means for copying the first font from the BIOS memory to a first memory of the data processing system to emulate the second font.

47. (previously presented) The data processing system of claim 45, wherein said means for emulating comprises:
means for stripping a portion from the first font.
48. (previously presented) The data processing system of claim 47, wherein the portion comprises a top line of the each character of the first font and a bottom line of each character of the first font.
49. (previously presented) The data processing system of claim 48, wherein the second font is of two lines of pixels shorter than the first font.
-